FORM PDP-1449 (based on Form PTO-1449)	ATTORNEY DOCKET NO.	SERIAL NO.
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PATENT AND TRADEMARK OFFICE
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Sheet 1 of 2

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U.S. PATENT DOCUMENTS

Ref. Desig.	Examiner's Initials	Document Number	Date	Name	Class/ Subclass	(If appropriate) Filing Date
1.	KP	5,699,551	12/1997	Taylor et al		
2.	KP	6,014,756	1/2000	Dottling et al		
3.	KP	6,151,664	11/2000	Borkenhagen et al		
4.	KP	6,182,196 B1	1/2001	DeRoo, John E.		
5.	KP	2003/0217230 A1	11/2003	rodriguez et al		
6.	KP	2004/0093465 A1	5/2004	Ramchandran, Amit		
7.	KP	6,732,235 B1	5/2004	Krivacek et al		
8.	KP	2004/0199718	10/2004	Byers et al		
9.	KP	6,820,170 B1	11/2004	Elnathan et al		
10.	KP	6,931,489 B2	8/2005	DeLano et al		
11.	KP	7,047,387 B2	5/2006	Goodsell, Andrew E.		
12.	KP	5,594,886	1/1997	Smith et al		

FOREIGN PATENT DOCUMENTS

Ref. Desig.	Examiner's Initials	Document Number	Date	Country	Class/ Subclass	Translation Yes No
1.						

OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, etc.)

Ref. Desig.	Examiner's Initials	
1.	KP	The Cache Memory Book, 2nd edition by Jim Handy, Pub. 1998, Pages 14-17, 42,47, 54-55 and 57.

Examiner: /Kaushikkumar Patel/ (11/30/2006)   Date Considered:

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OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, etc.)		
Ref. Desig.	Examiner's Initials	
2.	KP	Application-Driven Synthesis of Memory-Intensive Systems-on-Chip by Kirovski et al (IEEE Transactinos on Computer-aided design of integrated circuits and systems, Vol. 18, No. 9, September 1999, Pages 1316-1326.
3.	KP	Improving Direct-Mapped Cache Performance by the Addition of a Small Fully-Associative Cache and Prefetch Buffers; Norman P. Jouppi; Western Rearch Laboratory, Technical Note TN-14; March 1990; 46 pages

Examiner: /Kaushikkumar Patel/ (11/30/2006)   Date Considered:

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